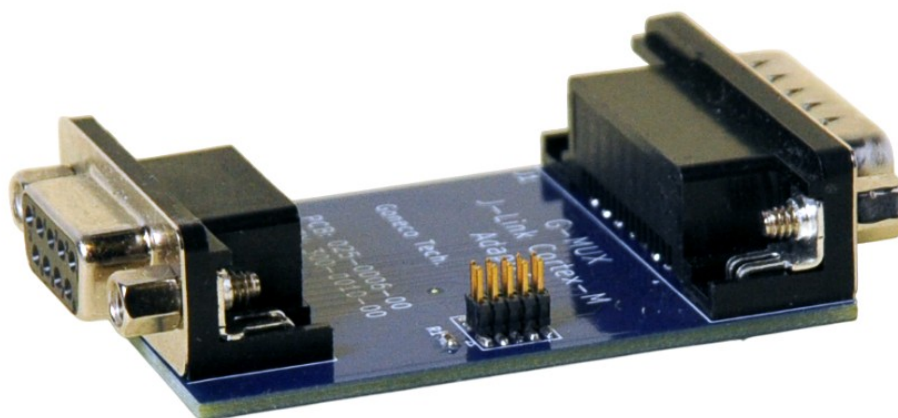


G-MUX Cortex-M J-Link Adapter

Technical Data Sheet



The Goneco Technologies G-MUX J-Link Adapter (Model Number JLA-2402) provides the ability to conveniently connect a J-Link Cortex-M programmer to the G-MUX without having to discretely wire the J-Link Programmer outputs to connector P1 of the G-MUX. Instead, the G-MUX J-Link Adapter provides a standard J-Link 10 pin header connector which is used to interface the J-Link Programmer to the G-MUX I/O ports on P1.

Note: This adapter does **not** connect the nRESET signal (which is not strictly required for normal operation) to any of the four I/O signals of the G-MUX connector P1. See section Connector pinout and description for the pinouts of the connectors on the adapter.

3D view



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1 Physical Characteristics

Parameter		
Width	40.4 mm	1.59 inches
Length	66 mm	2.6 inches
Height	18 mm	0.7 inches
Weight	70 grams	2.47 Ounces

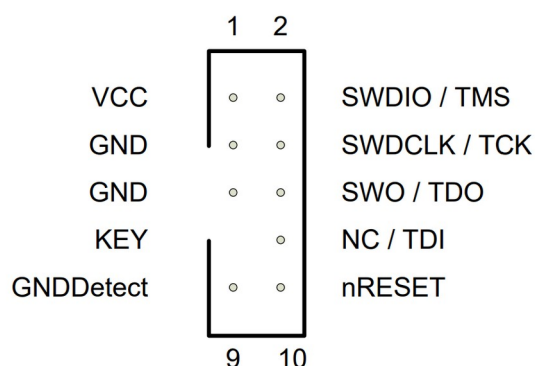
2 Environmental Conditions

Parameter		
Operating temperature	0 °C to +50 °C	
Storage temperature	-30 °C to +70 °C	
Humidity (operating)	5% to 90% relative humidity (% RH) at up to +30 °C, 5% to 60% RH above +30 °C up to +50 °C, non-condensing.	5% to 90% relative humidity (% RH) at up to +30 °C, 5% to 60% RH above +30 °C up to +50 °C, non-condensing.

3 Connector pinout and description

3.1 J-Link connector J2

The 9-Pin Cortex-M Adapter allows JTAG, SWD, and SWO connections between J-Link or Flasher and Cortex-M based target hardware systems. It is physically implemented as a 10-pin 0.05" Samtec FTSH connector.



Pin	Name	Description	Comments
1	Vcc	Supply voltage of the debug target	Connects to pin P1-10 (DIGITAL / ANALOG I/O #4 – COMMON) of the G-MUX
2	SWDIO / TMS		Connects to pin P1-2 (DIGITAL / ANALOG I/O #3 – COMMON) of the G-MUX
3	GROUND		Connects to pin P1-8 (GROUND) of the G-MUX.
4	SWDCLK / TCK		Connects to pin P1-9 (DIGITAL / ANALOG I/O #2 – COMMON) of the G-MUX. This signal has a 2k2 pullup to Vcc (pin 1 of this connector).
5	GROUND		Connects to pin P1-8 (GROUND) of the G-MUX.

6	SWO / TDO		Connects to pin P1-1 (DIGITAL / ANALOG I/O #1 – COMMON) of the G-MUX.
7	NOT CONNECTED		
8	NOT CONNECTED		
9	GROUND		Connects to pin P1-8 (GROUND) of the G-MUX.
10	NOT CONNECTED		

3.2 Connector J1 (D-SUB 15 pin male connector)

Mates with the female 15 pin connector P1 of the G-MUX.

3.3 Connector J3 (D-SUB 9 pin female connector)

Pin	Name	Description	Comments
1	Reserved		
6	MUX ENABLE		Connects to pin P1-11 (MUX ENABLE) of the G-MUX.
2	SELECT-2		Connects to pin P1-4 (SELECT-2) of the G-MUX.
7	SELECT-1		Connects to pin P1-12 (SELECT-1) of the G-MUX.
3	SELECT-0		Connects to pin P1-5 (SELECT-0) of the G-MUX.
8	Reserved		
4	Reserved		
9	Reserved		
5	GROUND		Connects to pin P1-8 (GROUND) of the G-MUX.